

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

In re Patent Application of:
GUINEA ET AL.

Serial No. Not yet assigned

Filing Date: Herewith

For: A DETECTOR FOR DETECTING
TIMING IN A DATA FLOW

"EXPRESS MAIL" MAILING LABEL NUMBER FL55165214645

DATE OF DEPOSIT August 10, 2000

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SUBMISSION OF PROPOSED MODIFICATIONS TO DRAWINGS

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Submitted herewith is a request for proposed drawing
modifications as indicated in red ink to label the blocks in
FIGS. 1-2 and 5-7.

Respectfully submitted,

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09/636099
08/10/00

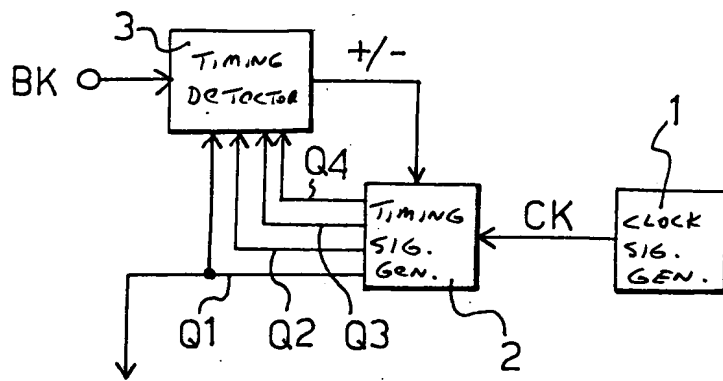


FIG. 1

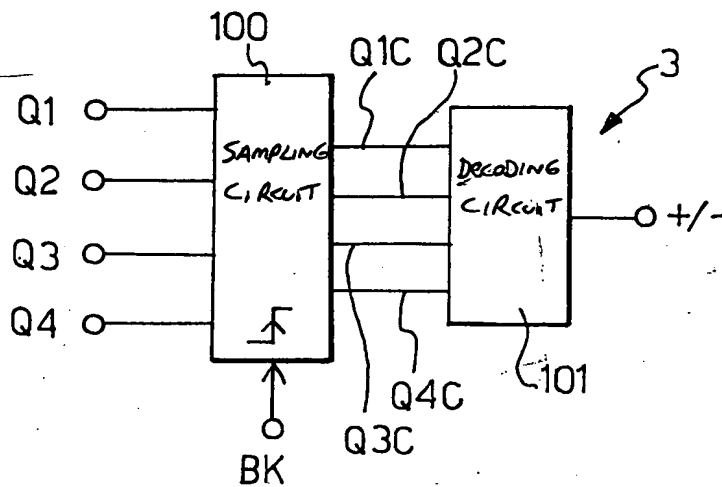


FIG. 2

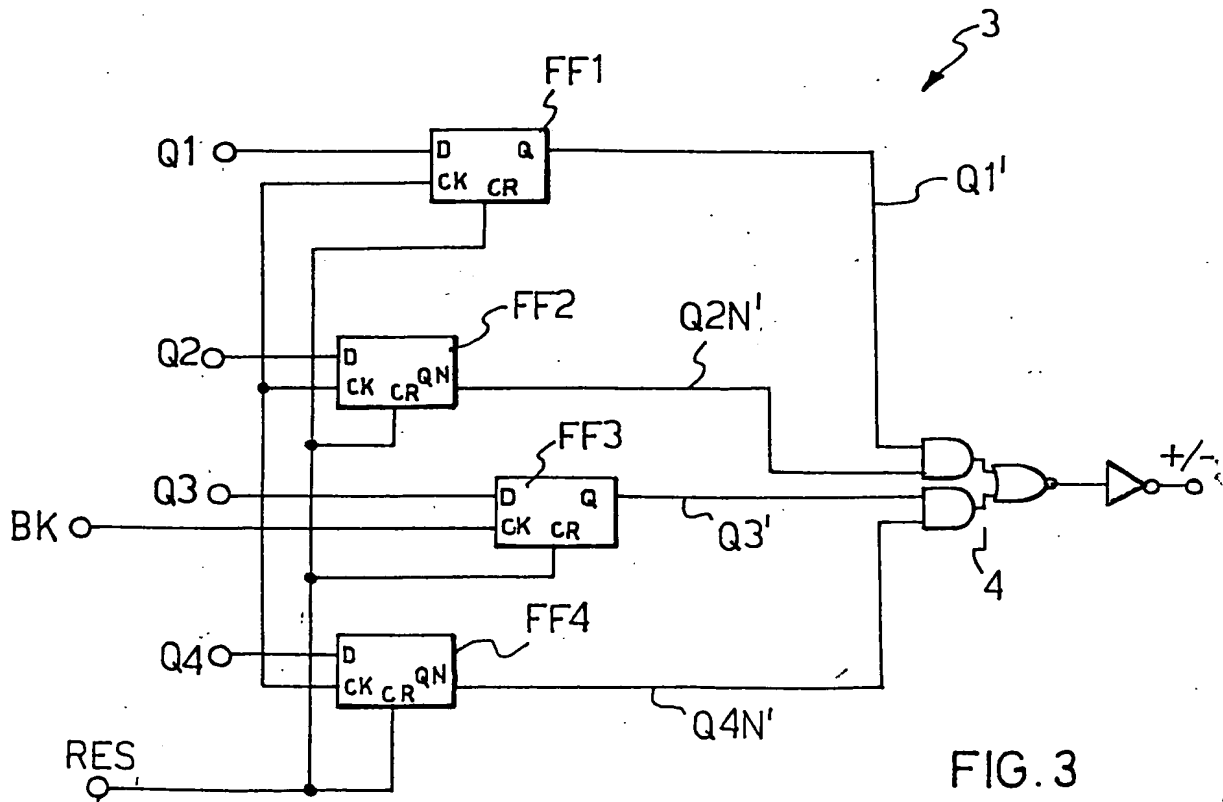


FIG. 3

The diagram illustrates a system architecture for remote digital circuitry. It consists of the following components and connections:

- Remote Digital Circuitry (9):** Represented by a vertical block on the left.
- Analog I/F (7):** A block connected to the Remote Digital Circuitry (9) via a bidirectional arrow.
- 6a, Rx:** The top receiver line of the transmission medium.
- 6b, Tx:** The bottom transmitter line of the transmission medium.
- 5:** The transmission medium connecting the Analog I/F (7) to the T/R I/F (5).
- T/R I/F (5):** A block connected to the transmission medium (5) via a bidirectional arrow.
- Digital Circuitry (8):** A block on the right connected to the T/R I/F (5) via a bidirectional arrow.

[illegible]

FIG. 6

[illegible]

FIG. 7